

Layer Stack Up Detail for: SU601140A.PcbDoc					
Layer	Material	Copper Thickness	Solder Thickness	Solder Material	Solder Type
Top Solder Mask	(.075)		0.4mil	Solder Resist	
Top Layer	(.67L)	1.4mil			
MidLayer1	(.6D)	1.4mil	12.6mil	FR-4	Core
MidLayer2	(.6D)	1.4mil	30.4mil	FR-4	PrePreg
Bottom Layer	(.68L)	1.4mil	12.6mil	FR-4	Core
Bottom Solder Mask	(.085)		0.4mil	Solder Resist	

DESIGN INFORMATION

BOARD SIZE: (REFER ALSO ARRAY/PANEL PROFILING INFORMATION)

5208MIL X 5464MIL

Number of Layers: 4

MIN. TRACK WIDTH: 4 MIL

MIN. CLEARANCE: 4 MIL

MIN. VIA PAD SIZE: 20 MIL

MINIMUM ANNULAR RING 0.05mm (2MIL) EXTERNAL

PER IPC-D-275 CLASS 2 LEVEL C

REGISTRATION TOLERANCES: METAL +/- .5 MIL, HOLES +/- .3 MIL

MATERIAL:

☐ FR-408 ☒ FR-4 High Tg ☐ OTHER

THICKNESS: ☒ 62 MIL (1.6mm) +/-10% ☐ OTHER

TOLERANCE: ☒ ANSI IPC-6012 TYPE 3 CLASS 2

☐ OTHER +/-

BOW & TWIST: ☒ ANSI IPC-6012 TYPE 3 CLASS 2

☐ OTHER +/-

COPPER THICKNESS (FINISHED):

OUTER: ☒ 1.4MIL (1oz) ☐ 2MIL (1.4oz) ☐ 2.8MIL (2oz)

INNER SIGNAL: ☒ 1.4MIL (1oz) ☐ 2.8MIL (2oz) ☐ N/A

DRILLING:

REFERENCE: ☒ AS SHOWN ☒ NC_DRILL FILES

PTH MIN COPPER THICKNESS: ☒ 1MIL ☐ OTHER

BOARD FINISH:

SILKSCREEN: ☒ TOP ☒ BOTTOM

SILKSCREEN COLOR: ☒ WHITE ☐ OTHER

SOLDER RESIST COLOR:

☒ GREEN ☐ BLUE ☐ OTHER

SURFACE FINISH: ☒ IMMERSION GOLD (ENG) ☐ ENERP

☐ IMM. TIN/SILVER OR EQUIV ☐ OTHER

ARRAY/PANEL: ☐ CUT AND TRM PER MECH LAYER 1

☐ N.C. ROUTE ☒ V. SCORE

CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF:

☒ ANSI IPC-A-600F CLASS -> ☐ 1 ☒ 2 ☐ 3

☒ UL 94V-0 ☒ RoHS ☐ OTHER PER ORDER

ADDITIONAL REQUIREMENTS:

MICROSECTION: ☐ YES

BARE BOARD ELEC. TEST: ☐ NONE ☒ REQUIRED ☐ PER ORDER

MANUFACTURER'S UL: ☐ RAL ☐ METAL ☒ SILK

PROJECT TITLE: LDC010LEUM

DESIGNED FOR: Public Release

FILE NAME: SU601140A.PcbDoc

ENGINEER: Ben Kasemsadeh

LAYOUT BY: Ben Kasemsadeh

SCALE: 1.08

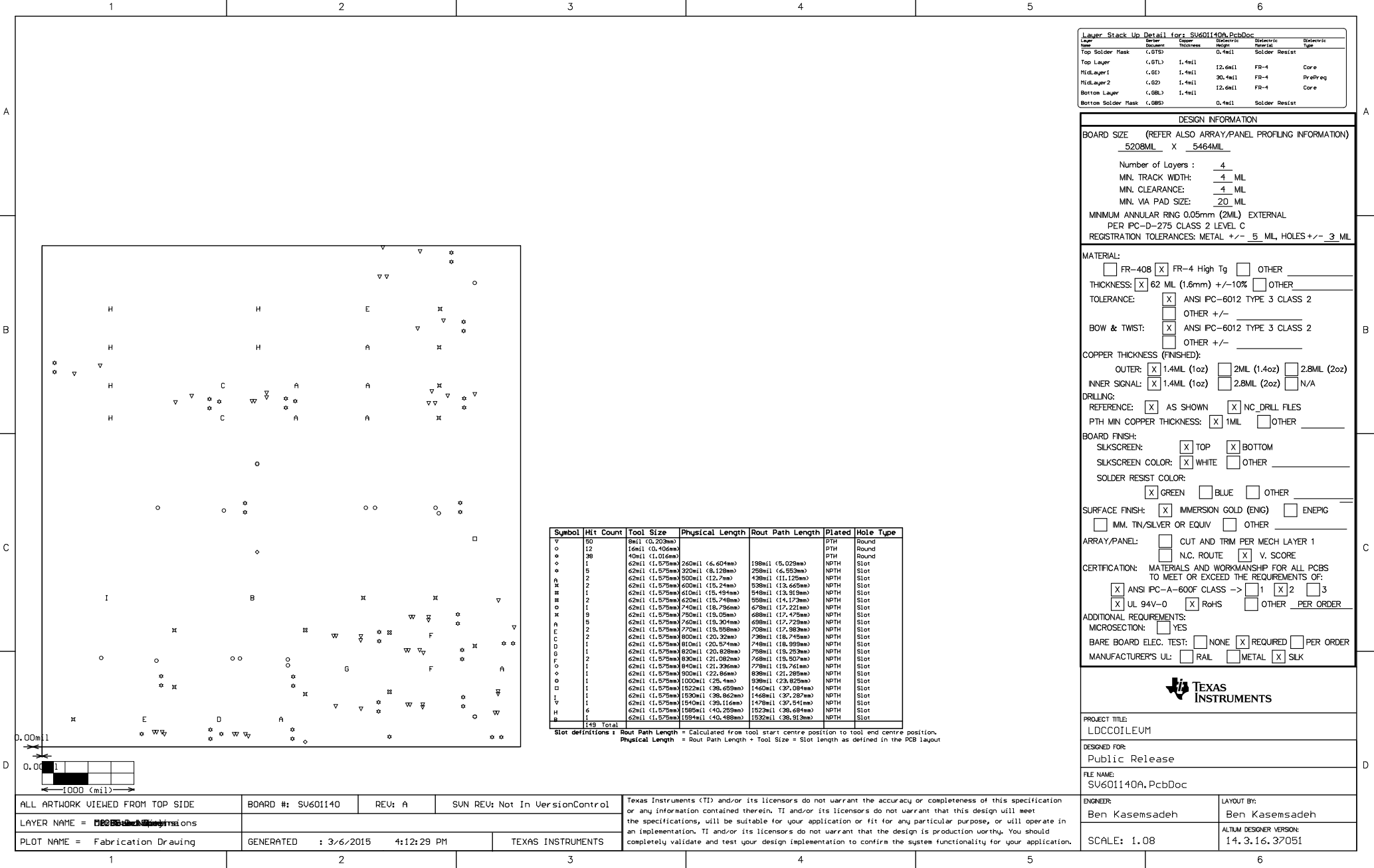
ALTIM DESIGNER VERSION: 14.3.16.37051

Symbol	Hit Count	Tool Size	Physical Length	Route Path Length	Plated	Hole Type
V	50	9mil (0.23mm)			PTH	Round
O	12	16mil (0.406mm)			PTH	Round
o	38	40mil (1.016mm)			PTH	Round
*	1	62mil (1.575mm)	260mil (6.604mm)	198mil (5.029mm)	NPTH	Slot
*	5	62mil (1.575mm)	350mil (8.128mm)	258mil (6.553mm)	NPTH	Slot
A	2	62mil (1.575mm)	500mil (12.7mm)	438mil (11.125mm)	NPTH	Slot
*	2	62mil (1.575mm)	600mil (15.24mm)	538mil (13.665mm)	NPTH	Slot
W	1	62mil (1.575mm)	610mil (15.494mm)	548mil (13.913mm)	NPTH	Slot
W	1	62mil (1.575mm)	620mil (15.748mm)	558mil (14.173mm)	NPTH	Slot
O	2	62mil (1.575mm)	740mil (18.796mm)	678mil (17.221mm)	NPTH	Slot
K	9	62mil (1.575mm)	750mil (19.05mm)	688mil (17.475mm)	NPTH	Slot
A	5	62mil (1.575mm)	760mil (19.304mm)	698mil (17.729mm)	NPTH	Slot
E	2	62mil (1.575mm)	770mil (19.558mm)	708mil (17.983mm)	NPTH	Slot
C	2	62mil (1.575mm)	800mil (20.32mm)	738mil (18.745mm)	NPTH	Slot
D	1	62mil (1.575mm)	810mil (20.574mm)	748mil (18.999mm)	NPTH	Slot
G	1	62mil (1.575mm)	820mil (20.828mm)	758mil (19.253mm)	NPTH	Slot
F	2	62mil (1.575mm)	840mil (21.082mm)	768mil (19.507mm)	NPTH	Slot
O	1	62mil (1.575mm)	840mil (21.082mm)	778mil (19.761mm)	NPTH	Slot
o	1	62mil (1.575mm)	900mil (22.86mm)	838mil (21.285mm)	NPTH	Slot
I	1	62mil (1.575mm)	1000mil (25.4mm)	938mil (23.829mm)	NPTH	Slot
D	1	62mil (1.575mm)	1022mil (26.05mm)	1460mil (37.084mm)	NPTH	Slot
I	1	62mil (1.575mm)	1030mil (26.162mm)	1468mil (37.287mm)	NPTH	Slot
V	1	62mil (1.575mm)	1540mil (39.116mm)	1478mil (37.541mm)	NPTH	Slot
H	6	62mil (1.575mm)	1585mil (40.255mm)	1522mil (38.604mm)	NPTH	Slot
A	1	62mil (1.575mm)	1594mil (40.488mm)	1532mil (38.913mm)	NPTH	Slot

149 Total

Slot definitions : Route Path Length = Calculated from tool start centre position to tool end centre position.

Physical Length = Route Path Length + Tool Size = Slot length as defined in the PCB layout



ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: SU601140	REV: A	SUN REV: Not In VersionControl	TEXAS INSTRUMENTS (TI) and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. TI and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. TI and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.
LAYER NAME = PCB Fabrication Drawings	GENERATED : 3/6/2015 4:12:29 PM	TEXAS INSTRUMENTS		
PLOT NAME = Fabrication Drawing				